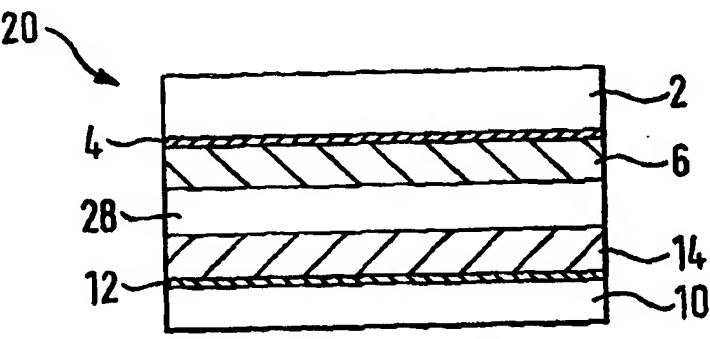


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(54) Title: MULTILAYER PHOTOVOLTAIC OR PHOTOCONDUCTIVE DEVICES			
(57) Abstract <p>The invention concerns optically absorptive photonic devices and in particular photovoltaic and photoconductive devices. It is particularly concerned with devices formed from multiple semiconducting layers, e.g. organic semiconducting polymers. Such a device has two central semiconductive layers which have been laminated together so as to form a mixed layer between the first and second semiconductive layers, while retaining at least some of the first and second semiconductive layers on either side of the mixed layer.</p>			
			

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Title of the InventionMULTILAYER PHOTOVOLTAIC OR PHOTOCONDUCTIVE DEVICESField of the Invention

The present invention relates to optically absorptive photonic devices and in particular photovoltaic and photoconductive devices and their formation. Embodiments of the invention relate particularly to devices formed from multiple semiconducting layers, preferably composed of organic semiconducting polymers.

Background to the Invention

Semiconductive photovoltaic devices are based on the separation of electron-hole pairs formed following the absorption of a photon. An electric field is generally used for the separation. The electric field may arise from a Schottky contact where a built-in potential exists at a metal-semiconductor interface or from a pn junction between p-type and n-type semiconductive materials. Such devices are commonly made from inorganic semiconductors especially silicon which is used in monocrystalline, polycrystalline or amorphous forms. Silicon is normally chosen because of its high conversion efficiencies and the large industrial investments which have already been made in silicon technology. However, silicon technology has associated high costs and complex manufacturing process steps resulting in devices which are expensive in relation to the power they produce.

"Two-layer organic photovoltaic cell", Applied Physics Letters 48(2), 13th January 1986, C.W. Tang, US 4,164,431 and US 4,281,053 describe multi-layer organic photovoltaic elements. These devices are formed in a layer by layer fashion. A first organic semiconductive layer is deposited on an electrode, a second organic semiconductive layer is deposited on the first organic layer and an electrode is deposited on the second organic

layer. The first and second organic semiconductive layers are electron acceptors and hole acceptors. In the following, an "electron accepting material" refers to a material which due to a higher electron affinity compared to another material is capable of accepting an electron from that material. A "hole accepting material" is a material which due to a smaller ionisation potential compared to another material is capable of accepting holes from that other material. The absorption of light in organic photoconductive materials results in the creation of bound electron-hole pairs, which need to be dissociated before charge collection can take place. The material considerations for organic devices are different compared to inorganic devices, where the electron and holes created by the absorption of a photon are only weakly bound. The dissociation of the bound electron-hole pair is facilitated by the interface between the layer of material which acts as a hole acceptor and the layer of semiconductive material which acts as an electron acceptor. The holes and electrons travel through their respective acceptor materials to be collected at the electrodes.

The designing of photovoltaic devices which are fabricated in a layer by layer fashion is limited. When one organic layer is deposited on top of another organic layer, the second layer must be added in such a way that the previously deposited layer is not affected in a detrimental way. Consequently solvents used for subsequent layers are limited in order not to dissolve the previous layer completely or destroy it in other ways.

"Efficient photodiodes from interpenetrating polymer networks", Nature, vol 376, 10th August 1995, page 498-500, J.J.M. Halls et al, and US 5,670,791 describe the formation of a photovoltaic device by depositing a single layer comprising a blend of first and second semiconductive polymers and the deposition of a second electrode on top of that layer. The first semiconductive polymer acts as a electron acceptor and the second semiconductive polymer acts as a hole acceptor. The first and second semiconductive

polymers form respective continuous networks that interpenetrate so that there is a continuous path through each of the semiconductive polymers and a charge carrier within one of the first and second semiconductive polymers can travel between the first and second electrodes without having to cross into the other semiconductive polymer. However, these devices do not show the high efficiency that would be expected if the devices worked as ideally envisaged. This may be due to the fact that it is likely that at least one of the polymers can extend through the whole device, thereby creating a parallel system of single material diodes.

Summary of the Invention

It is an object of the present invention to provide an improved photovoltaic device.

According to a first aspect of the invention there is provided a method of forming a photovoltaic or photoconducting device comprising the laminating together of a first component having a first electrode and a first semiconductive layer predominantly comprising a first semiconductive material, and a second component having a second electrode and a second semiconductive layer predominantly comprising a second semiconductive material, wherein the laminating step involves the controlled joining of said first semiconductive layer and said second semiconductive layer to form a mixed layer comprising proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer while retaining said first and second semiconductive layers with a reduced thickness.

According to another aspect of the invention there is provided a method of designing and creating a photovoltaic or photoconducting device comprising the steps of: choosing first and second semiconductive materials on the basis of their

electronic properties so that said first semiconductive material acts as an electron donor and said second semiconductive material acts as an electron acceptor; forming a first component comprising a first electrode and a first semiconductive layer predominantly comprising said first semiconductive material; forming a second component comprising a second electrode and a second semiconductive layer predominantly comprising said second semiconductive material; and joining the first component to the second component by laminating said first semiconductive layer to said second semiconductive layer. The laminating step may involve the controlled joining of said first semiconductive layer and said second semiconductive layer to form a mixed layer comprising proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive layer while retaining said first and second semiconductive layers with a reduced thickness.

Laminating may comprise the application of pressure or heat or pressure and heat. If heat is applied it may involve heating one or both of the semiconductive layers above their glass transition temperatures. The semiconductive layers may be individually treated before lamination, for example by organic or inorganic doping. Such treatment may vary the morphology, the light-absorption characteristics, the transport properties or the injection properties of one or both semiconductive layers. The thickness of the semiconductive layers before lamination may be controlled, for example by spin coating a solution of semiconductive material. Furthermore the thickness of the mixed layer and/or the thickness of the first and second semiconductive layers remaining may be controlled, for example by annealing.

According to a further aspect of the invention there is provided a photovoltaic or photoconducting device comprising: a first electrode; a first semiconductive layer, predominantly comprising a first semiconductive material, over at least part of said first electrode; a mixed layer over said first semiconductive layer; a second semiconductive layer, predominantly comprising a second

semiconductive material, over said mixed layer; and a second electrode over at least part of said second semiconductive layer, wherein said mixed layer is connected with the first and second semiconductive layers and has proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer.

A first substrate may carry or comprise said first electrode and a second substrate may carry or comprise said second electrode. The first and second substrates are preferably self-supporting.

According to a still further aspect of the invention there is provided a photovoltaic or photoconducting device comprising: a first substrate carrying or comprising a first electrode and carrying a first semiconductive layer predominantly comprising a first semiconductive material; a second substrate carrying or comprising a second electrode and carrying a second semiconductive layer predominantly comprising a second semiconductive material; and a third mixed layer, arranged between and connected with the first and second semiconductive layers and having proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer.

According to any one of the different aspects of the invention, the mixed layer may be an interpenetrating network of the first and second semiconductive materials. The first, and likewise the second, semiconductive materials may comprise a mixture of component materials or a single component material. The first and second substrates and the first and second components may be self-supporting. The semiconductive materials may have the properties as defined in any one of the claims 20 to 36. The first and second semiconductive layers may have the properties, before and after lamination, as defined by claims 38 to 43. The

first electrode may make physical contact with the first semiconductive layer, or one of a plurality of layers may be interposed between the first electrode and the first semiconductive layer. Likewise the second electrode may make physical contact with the second semiconductive layer, or one of a plurality of layers may be interposed between the second electrode and the second semiconductive layer. The electrodes may have the same or different work functions. An electrode may itself form one of the self-supporting substrates, or the electrode may be comprised in or carried by one of the self supporting substrates. Preferably one or both of the substrates transmit light. Furthermore, one or both of the substrates (and components) may be flexible.

Lamination techniques are well established, allowing devices to be made in a straightforward way, on a large scale, and at low costs.

The present invention provides for the treatment of the first semiconductive layer and/or second semiconductive layer before effecting lamination. This treatment was not possible in the prior art.

A device made according to the present invention is believed to help avoid the creation of a parallel system of diodes and gives improved performance over the device described in the previously referred to disclosure by Halls et al. The first and second semiconductive layers ensure that a single material does not extend from the first electrode to the second electrode.

A device made according to the present invention has a high efficiency to cost ratio.

Devices made according to the present invention have a high efficiency compared to the previously reported polymer blend device.

Devices made according to the invention make it possible to minimise the risk of forming pin holes or direct conductive paths from one electrode to the other. This is particularly advantageous in the production of large area devices.

As the device is not created in a layer by layer fashion the choice of suitable materials is increased as the effects of creating the second semiconductive layer over the first semiconductive layer in a layer by layer fashion are less critical. This affords greater flexibility in tailoring the device to absorb in specific wavelength regions and allows for a more efficient use of the spectrum when used as a solar cell. It also allows other properties of the device to be controlled or improved, such as its conductivity and serial resistance.

For a better understanding of the present invention and to understand how the same may be put into effect, reference will now be made by way of example only to the enclosed drawings.

Brief Description of the Drawings

Figure 1 is the chemical structure of the organic polymer POPT;

Figure 2 is the chemical structure of the organic polymer MCP;

Figure 3 is the chemical structure of the organic polymer P3HT;

Figures 4a, 4b and 4c exemplify the method of the invention;

Figure 5 is an exemplary structure of a device according to the invention; and

Figure 6 is a diagram showing apparatus which is suitable for performing the method of the invention according to certain embodiments.

Description of the Preferred Embodiment

Figures 4a, 4b, 4c and Figure 5 illustrate the manufacture of a

photovoltaic or photoconducting device 20. The device 20 has a first component part 8 and a second component part 16 which are laminated together as illustrated in Figure 4c. The first component part 8 is illustrated in Figure 4a and has a first self-supporting substrate 2, a first electrode 4 and a first semiconductive layer 6. The second component part 16 is illustrated in Figure 4b and has a second self-supporting substrate 10, a second electrode 12 and a second semiconductive layer 14. On lamination, a mixed layer 28 containing material from the first and second semiconductive layers is formed at the interface of the first semiconductive layer 6 and the second semiconductive layer 14 as illustrated in Figure 5.

The material of the first semiconductive layer 6 acts as an electron donor while the material of the second semiconductive layer 14 acts as an electron acceptor in this material combination. Semiconducting polymers which can act as electron acceptors are e.g. polymers, containing CN- or CF₃ groups like CN-PPV, MEH-CN-PPV, CF₃ substituted ones or Buckminsterfullerene (C₆₀) alone or functionalised to enhance solubility. Semiconducting polymers which do not contain such or other electron withdrawing groups can often act as hole acceptors, for instance the following polymers (and their derivatives) or copolymers containing units of the following polymers (and derivatives): poly(phenylene), poly(phenylene vinylene), poly(thiophene), poly(silane), poly(thienylene vinylene) and poly(isothianaphthene).

Other suitable semiconductive materials include: organometallic polymers; phthalocyanines, perylenes, naphthalocyanines, squaraines, merocyanines and their respective derivatives; and azo-dyes consisting of azo chromophore (-N=N-) linking aromatic groups.

Other suitable semiconductive materials include perylene polymer, poly(squaraines) and organic molecules. Examples of semiconductive organic molecules include dyes and pigments, as

described in US 4,281,053, US 4,164,431, US 5,201,961, and US 5,350,459.

The semiconductive layers may be formed from a blend of semiconductive materials including blends of polymers with polymers and blends of polymers with molecules.

The first substrate 2 and first electrode 4 and/or the second electrode 12 and the second substrate 10 are transparent to allow light to reach the mixed layer. On illumination the device is capable of supplying either electric power or - under applied bias voltage - a light dependent current.

Generally the electrodes have different work functions in order to induce an electric field across the device. However, when the device is used under reverse bias (externally applied voltage), the electrodes may have the same work function and be made of the same material. Examples of high work function materials are: Au, Ag, Co, Ni, Pt, C, doped poly(aniline), doped poly(ethylene dioxythiophene) and other poly(thiophene) derivatives, doped poly(pyrrole), indium tin oxide, fluorinated tin oxide, tin oxide and zinc oxide. Examples of low work function materials are Li, In, Al, Ca, Mg, Sm, Tb, Yb, Zr, and alloys of these. If a metal electrode is used, the metal itself can form both the self-supporting substrate and the electrode. An example of this is aluminium foil.

In the final device 20, the first and second semiconductive layers 6 and 14 are thick enough to prevent the mixed layer 28 being in direct contact with the electrodes but should otherwise be as thin as possible.

Although in the Figures 4a,, 4b, 4c and 5 the first semiconductive layer 6 has been shown in physical contact with the first electrode 4 and the second semiconductive layer 14 has been shown in physical contact with the second electrode 12, such physical contact is not necessary for the operation of the

device. One or more intermediate layers may lie between the first electrode 4 and first semiconductive layer 6. Likewise, one or more intermediate layers may lie between the second electrode 12 and the second semiconductive layer 14. These intermediate layers may be a layer of doped poly(ethylene dioxythiophene), or poly(aniline) or a doped conjugated polymer. These layers are particularly useful on top of an indium tin oxide electrode where they protect the semiconducting layer from oxygen and other impurities emerging from the indium tin oxide. Other examples of intermediate layer materials are polymers incorporating triphenylene units which enhance hole transport and tris(8-quinolinato) aluminium (III) complexes (Alq_3) which enhances electron transport.

Embodiment 1

A first method of forming the device 20 will be explained with reference to Figure 4a. A glass substrate 2 is covered with indium tin-oxide, ITO, to form the first electrode 4. The ITO surface is cleaned using acetone and methanol. An organic polymer solution is prepared by dissolving 10 milligrams of regioregular POPT (poly(3-(4-octylphenyl)thiophene)), the chemical structure of which is illustrated in Figure 1, in 2 millilitres of chloroform. The solution is filtered with 0.45 micrometre filters and then spincoated onto the ITO surface to give a thickness of between 40 and 150 nm. The polymer covered substrate is then heated from room temperature to 230°C at a rate of 4°C per minute and maintained at 230°C for 30 minutes. This heating occurs in a vacuum chamber with a gas pressure of below 10^{-5} torr and induces a phase transition in the POPT which shifts its absorption to longer wavelengths.

Referring to Figure 4b, the formation of a second component part 16 will be described. The second electrode 12 is formed on the second substrate 10 by thermally evaporating aluminium onto a glass substrate. The second semiconductive layer 14 is formed over the aluminium electrode 12 by spincoating an organic polymer

solution onto the aluminium coated substrate. The solution is formed by dissolving 10mg of MCP (poly(2,5-bis(nitrilemethyl)-1-methoxy-4-(2'-ethyl-hexyloxy)benzene-co-2,5-dialdehyde-1-methoxy-4-(2'-ethylhexyloxy)benzene)), the structure of which is illustrated in Figure 2, in 2 millilitres of chloroform and filtering the solution using 0.45 micrometre filters. The formation of the aluminium electrode 12 and the MCP semiconductive layer 14 is carried out in an inert gas atmosphere to avoid oxidation of the aluminium contacts.

After the individual manufacture of the first component part 8 and the second component part 16 they are laminated together to form the device 20 as indicated diagrammatically in Figure 4c by the arrows A. The first component part 8, while at its elevated temperature, is aligned with the second component part so that the POPT semiconductive layer 6 and the MCP semiconductive layer 14 are opposed. The semiconductive layers are brought into contact and a pressure of approximately 30 kPa is applied for two to four minutes to laminate the component parts together. During lamination the POPT semiconductive layer 6 is at a temperature of approximately 230°C which is above the glass transition temperature of POPT.

The thickness of the POPT layer 6 and the MCP layer 14, before lamination, can be controlled by varying the rotational speed at which the spincoating occurs. The film thickness when spincoating a solution is also determined by the solution's concentration, the temperature and the solvent used.

Figure 5 illustrates the structure resulting from the lamination process. The POPT homolayer 6 and the MCP homolayer 14 interact to form the mixed layer 28. This layer comprises a mixture of POPT derived from the POPT layer 6 and MCP derived from the MCP layer 14. The first semiconductive layer 6 formed from POPT acts as a hole acceptor and the second semiconductive layer formed from MCP acts as an electron acceptor.

The first semiconductive layer 6 can alternatively be formed from P3HT (regioregular poly(3-hexylthiophene)), the structural formula of which is illustrated in figure 3. 10 milligrams of this polymer is dissolved in 2 millilitres of chloroform and then filtered using 0.45 micrometre filters to create a polymer solution. This solution is spincoated over the ITO electrode 4. P3HT does not show the phase transition that is found in POPT. However, the first component part 8 is heated above its glass transition temperature to approximately 200°C and laminated with the second component part 16 in the manner previously described. In the resulting device 20, the mixed layer 28 is a mixture of P3HT and MCP.

As another alternative to the use of MCP, a cyano-substituted poly(phenylene vinylene) derivative without the methyl-ethylhexyloxy group could be used as electron accepting material in the second semiconductive layer 14 with either a poly(thiophene) derivative or a poly(phenylene vinylene) derivative as hole accepting material in the first semiconductive layer 6.

Embodiment 2

In the second embodiment the first semiconductive layer 6 and the second semiconductive layer 14 are formed by a different method. Referring to Figure 4a, the first semiconductive layer 6 is a polymer blend formed by dissolving 19 milligrams of POPT and 1 milligram of MCP in 4 millilitres of chloroform, filtering the solution using 0.45 micrometre filters, and spincoating the filtered solution on top of the indium tin oxide electrode 4. The second semiconductive layer 14 of the second component part 16 is also a polymer blend. This polymer blend is formed by dissolving 1 milligram of POPT and 19 milligrams of MCP in 4 millilitres of chloroform and filtering the solution using 0.45 micrometre filters. The polymer blend is then spincoated onto the aluminium electrode 12. The method is then the same as previously described. The first component part 8 is heated and

the two component parts are laminated together to form the complete device 20.

In the first semiconductive layer 6, POPT predominates and the preferred ratio of POPT to MCP is 95% to 5% by weight. The efficiency of the finished device decreases as the percentage of MCP to POPT increases, however, good results are still obtained with a ratio of 80% POPT to 20 MCP by weight.

Likewise, in relation to the second semiconductive layer 14 MCP predominates and the preferred ratio of MCP to POPT is 95% to 5% by weight but it may also vary and good results are achieved with a ratio of 80% MCP to 20% POPT by weight.

Figure 5 illustrates the structure resulting from the lamination process. The first blended semiconductive layer 6 and the second blended semiconductive layer 14 interact to form the mixed layer 28. This layer comprises a mixture derived from the first blended layer 6 and the second blended layer 14. The mixed layer 28 has a smaller %POPT than the first blended layer 6 and a smaller %MCP than the second blended layer 14. POPT acts as a hole acceptor and MCP acts as an electron acceptor.

As an alternative to the use of POPT and MCP, POPT and MCP may be respectively replaced by a poly(thiophene) derivative and a cyano-substituted poly(phenylene vinylene) derivative.

According to the described methods, the separate first and second semiconductive layers can be individually treated before lamination. Such treatment may involve the inducement of a phase transition in a semiconductive polymer to vary its absorption characteristics, the ordering of the material to improve its transport properties or the doping of the material. Separate annealing of the two components 8 and 16 before their lamination together allows traces of solvent, water and oxygen to be removed. Selective doping (with molecular, polymeric or inorganic dopants) of each layer can represent a very powerful

means to decrease the serial resistance and/or create or enhance internal electric fields. The bandgap of the semiconductor layers may be decreased or even removed depending on the degree of doping. Possible dedoping (neutralisation) at the interface after the lamination of both substrates may lead to the (re)creation or change of a bandgap and/or transport properties which could enhance the efficiency of such a device. This has partly been discussed in Synthetic Metals 84 (1997) 477-482, Yoshino et al. With the present invention the donor and the acceptor material - and any underlying layers can be separately doped and optimised.

The mixed layer is formed by the diffusion of the first semiconductive layer into the second semiconductive layer, accordingly, the device 2 may be annealed after lamination in order to control the phase separation and the thickness of the mixed layer 16. This provides for an increase of the interfacial area in the finished device. The increase of interfacial area in the finished device between the material of the first semiconductive layer 6 and the material of the second semiconductive layer 14 can be significantly enhanced compared to a device produced by the deposition of layer upon layer.

In the preceding described methods solutions for coating the first and second semiconductive layers were made with a polymer to solvent ratio of 5 milligrams to 1 millilitre. However, this range is dependent upon the solubility of the polymer in the solution and can range from 0.1 milligrams per millilitre to 75 milligrams per millilitre, depending on the type of polymer used.

As an alternative to what has previously been described, the first substrate 2 and the second substrate 10 are formed from flexible plastics material. The first substrate 2 is heat stabilised polyester (PET) which is available commercially ready coated with ITO. The first electrode may alternatively be formed on the polyester substrate 2 by depositing indium tin oxide or forming a conductive polymer. To form a conductive polymer, a

solution of poly(ethylene dioxythiophene)/polystyrene sulfonic acid is spin coated over the polyester substrate 2. A suitable solution is commercially available from Bayer AG, Germany. The electrically conducting polymer film makes a transparent electrode onto which the first semiconductive layer may be spin coated. The first semiconductive layer 6 is then formed over the electrode 4 as previously described in relation to Figure 4. The second substrate 10 is also a heat stabilised polyester film. The second electrode 12 is formed by thermally evaporating a thin layer of aluminium onto the polyester film and the second semiconductive layer 14 is formed as previously described.

Figure 6 illustrates an apparatus suitable for carrying out lamination of the first semiconductive layer to the second semiconductive layer. The first component part 8 can be supplied as a self-supporting film 2 carrying a first indium tin oxide electrode 4 and a first semiconductive layer 6 from a roll of film 22. The second component part 16 can be supplied as a self-supporting film 10 carrying a second aluminium electrode 12 and a second semiconductive layer 14 from a roll of film 24. The two self-supporting, coated films 8 and 16 are supplied to a pair of heated rollers 26 which laminate the films together to produce a continuous laminated multi-layer structure as illustrated in Figure 5.

Alternatively, one or both of the flexible substrates 2 and 10 can be supplied from a roll. A substrate coming off the roll is continuously coated to form a component part. This may require the continuous sequential deposition of a layer to form an electrode followed by a semiconductive layer or, if the substrate already has an electrode, the continuous deposition of a semiconductive layer. One or both of the component parts can thus be continuously supplied to a pair of heated rollers 26 which laminate the component parts together to produce a continuous laminated multilayer structure as illustrated in Figure 5.

The flexible substrates 2 and 10 may be formed from heat stabilised poly(ethylene terephthalate), poly(imide), poly(ether imide), poly(ethylene naphthalate), poly(carbonate), thin flexible sheets of glass and metal foil.

Although only a spin coating technique has been described for applying the first and second semiconductive layers onto the first and second components respectively it should be appreciated that different techniques can be used for example: spray coating, dip coating, roll coating, bead coating, meniscus coating, Langmuir-Blodgett techniques, screen printing and self-assembly techniques.

CLAIMS:

1. A method of forming a photovoltaic or photoconducting device comprising the laminating together of a first component having a first electrode and a first semiconductive layer predominantly comprising a first semiconductive material, and a second component having a second electrode and a second semiconductive layer predominantly comprising a second semiconductive material, wherein the laminating step involves the controlled joining of said first semiconductive layer and said second semiconductive layer to form a mixed layer comprising proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer while retaining said first and second semiconductive layers with a reduced thickness.
2. A method of designing and creating a photovoltaic or photoconducting device comprising the steps of:
 - choosing first and second semiconductive materials on the basis of their electronic properties so that said first semiconductive material acts as an electron donor and said second semiconductive material acts as an electron acceptor;
 - forming a first component comprising a first electrode and a first semiconductive layer predominantly comprising said first semiconductive material;
 - forming a second component comprising a second electrode and a second semiconductive layer predominantly comprising said second semiconductive material; and
 - joining the first component to the second component by laminating said first semiconductive layer to said second semiconductive layer.
3. A method as claimed in claim 2, wherein the laminating step involves the controlled joining of said first semiconductive layer and said second semiconductive layer to form a mixed layer comprising proportionally less of said first semiconductive

material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer.

4. A method as claimed in any preceding claim, wherein the laminating step comprises the application of pressure and/or heat.

5. A method according to claim 4, wherein said application of heat comprises heating at least one of said first and second semiconductive layers above their glass transition temperature.

6. A method as claimed in any preceding claim, wherein the laminating step further comprises annealing to form a mixed layer of desired thickness.

7. A method as claimed in any preceding claim, further comprising the treatment of at least one of said first or second semiconductive layers before lamination by organic or inorganic doping.

8. A method according to any preceding claim, further comprising, before said lamination step, the treatment of at least one of said first or second semiconductive layers to vary their light-absorption characteristics and/or transport and injection properties.

9. A method as claimed in any preceding claim, wherein the thickness of said first semiconductive layer before lamination and the thickness of said second semiconductive layer before lamination are controlled.

10. A method as claimed in any preceding claim, wherein said first component is self-supporting and comprises first semiconductive material over a self-supporting substrate carrying or comprising a first electrode.

11. A method as claimed in any preceding claim, wherein said second component is self-supporting and comprises second semiconductive material over a self-supporting substrate carrying or comprising a first electrode.
12. A method as claimed in any preceding claim, wherein at least one of the components is flexible and stored as a roll and supplied therefrom for lamination with the other self-supporting substrate.
13. A photovoltaic or photoconducting device comprising:
a first electrode;
a first semiconductive layer, predominantly comprising a first semiconductive material, over at least part of said first electrode;
a mixed layer over said first semiconductive layer;
a second semiconductive layer, predominantly comprising a second semiconductive material, over said mixed layer; and
a second electrode over at least part of said second semiconductive layer,
wherein said mixed layer is connected with the first and second semiconductive layers and has proportionally less of said first semiconductive material than said first semiconductive layer and proportionally less of said second semiconductive material than said second semiconductive layer.
14. A photovoltaic or photoconducting device as claimed in claim 13, wherein a first substrate carries or comprises said first electrode and a second substrate carries or comprises said second electrode.
15. A photovoltaic or photoconducting device as claimed in claim 14, wherein said first and second substrates are self-supporting.
16. A device as claimed in claim 14 or 15 or a method as claimed in claim 10 or 11, wherein at least one of said first and second

substrates is flexible.

17. A device as claimed in any one of claims 14 to 16 or a method as claimed in any one of claims 10 or 11 or 16, wherein the first or/and second electrode itself forms the first or/and second substrates respectively.

18. A device as claimed in claim 17 or a method as claimed in claim 17, wherein one of the substrates is a metal foil.

19. A device as claimed in any one of claims 14 to 18 or a method as claimed in any one of claims 10, 11 and 16 to 18, wherein the first and/or second substrate is arranged for the transmission of light.

20. A device as claimed in any one of claims 13 to 19 or a method as claimed in any one of claims 1 to 12 and 16 to 19, wherein at least one of said first semiconductive material and said second semiconductive material comprise a mixture of component materials.

21. A device as claimed in any one of claims 13 to 20 or a method as claimed in any one of claims 1 to 12 and 16 to 20, wherein at least one of said first semiconductive material and said second semiconductive material comprise a single component material.

22. A device as claimed in any one of claims 13 to 21 or a method as claimed in any one of claims 1 to 12 and 16 to 21, wherein said first and second semiconductive materials are respectively hole acceptors and electron acceptors.

23. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein at least one of the first and second semiconductive materials comprises an organic semiconductor.

24. A device as claimed in any one of claims 13 to 23 or a method as claimed in any one of claims 1 to 12 and 16 to 23, wherein at least one of said first and said second semiconductive materials comprises a semiconductive polymer.
25. A device as claimed in claim 24 or a method as claimed in claim 24, wherein at least one of said first and second semiconductive materials comprises a conjugated polymer.
26. A device as claimed in claim 25 or a method as claimed in claim 25, wherein at least one of the organic conjugated polymers is chosen from the group comprising:
poly(phenylene) and derivatives, poly(phenylene vinylene) and derivatives, poly(thiophene) and derivatives, poly(thienylenevinylene) and derivatives and poly(isothianaphthene) and derivatives.
27. A device as claimed in claim 24 or a method as claimed in claim 24, wherein at least one of the polymers is a poly(squaraine) or a derivative thereof.
28. A device as claimed in claim 24 or a method as claimed in claim 24, wherein at least one of the polymers is a polymer containing perylene units.
29. A device as claimed in claim 23 or a method as claimed in claim 23, wherein at least one of the semiconductive materials comprises an organic pigment or dye.
30. A device as claimed in claim 24 or a method as claimed in claim 24, wherein at least one of the semiconductive materials comprises an organometallic polymer.
31. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein at least one of the semiconductive materials comprises material chosen from the group comprising phthalocyanines,

perylene, naphthalocyanines, squaraines, merocyanines and their respective derivatives.

32. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein at least one of the semiconductive materials comprises an azo-dye consisting of azo chromophore (-N=N-) linking aromatic groups.

33. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein at least one of the semiconductive materials comprises a poly(silane), or poly(germanate).

34. A device as claimed in any one of claims 13 to 33 or a method as claimed in any one of claims 1 to 12 and 16 to 33, wherein the first or/and the second semiconductive material is doped with organic or inorganic dopants.

35. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein the first semiconductive material comprises POPT and said second semiconductive material comprises MCP.

36. A device as claimed in any one of claims 13 to 22 or a method as claimed in any one of claims 1 to 12 and 16 to 22, wherein said first semiconductive material comprises P3HT and said second semiconductive material comprises MCP.

37. A device as claimed in any one of claims 13 to 36 or a method as claimed in any one of claims 1 to 12 and 16 to 36, wherein said mixed layer is an interpenetrating network of said first and second semiconductive materials.

38. A device as claimed in any one of claims 13 to 37 or a method as claimed in any one of claims 1 to 12 and 16 to 37, wherein said first semiconductive layer is more than 80% (by

weight) said first semiconductive material.

39. A device as claimed in claim 38 when dependent upon claim 21 or a method as claimed in claim 38 when dependent upon claim 21, wherein said first semiconductive layer is a homolayer of said first semiconductive material.

40. A device as claimed in claim 38 or a method as claimed in claim 38, wherein said first semiconductive layer also contains said second semiconductive material.

41. A device as claimed in any one of claims 13 to 40 or a method as claimed in any one of claims 1 to 12 and 16 to 40, wherein said second semiconductive layer is more than 80% (by weight) said second semiconductive material.

42. A device as claimed in claim 41 when dependent upon claim 21 or a method as claimed in claim 41 when dependent upon claim 21, wherein said second semiconductive layer is a homolayer of said second semiconductive material.

43. A device as claimed in claim 41 or a method as claimed in claim 41, wherein said second semiconductive layer also contains said first semiconductive material.

44. A device as claimed in any one of claims 13 to 43 or a method as claimed in any one of claims 1 to 12 and 16 to 43, wherein one or a plurality of layers is between said first electrode and said first semiconductive layer.

45. A device as claimed in any one of claims 13 to 43 or a method as claimed in any one of claims 1 to 12 and 16 to 43, wherein said first electrode is in physical contact with said first semiconductive layer.

46. A device as claimed in any one of claims 13 to 45 or a method as claimed in any one of claims 1 to 12 and 16 to 45,

wherein one or a plurality of layers is between said second electrode and said second semiconductive layer.

47. A device as claimed in any one of claims 13 to 45 or a method as claimed in any one of claims 1 to 12 and 16 to 45, wherein said second electrode is in physical contact with said second semiconductive layer.

48. A device as claimed in any one of claims 13 to 47 or a method as claimed in any one of claims 1 to 12 and 16 to 47, wherein said first electrode has a higher work function than said second electrode.

49. A device as claimed in any one of claims 13 to 48 or a method as claimed in any one of claims 1 to 12 and 6 to 48, wherein said first electrode comprises indium tin oxide and said second electrode comprises aluminium.

50. A device as claimed in any one of claims 13 to 47 or a method as claimed in any one of claims 1 to 12 and 6 to 47, wherein said first and second electrodes have substantially the same work function.

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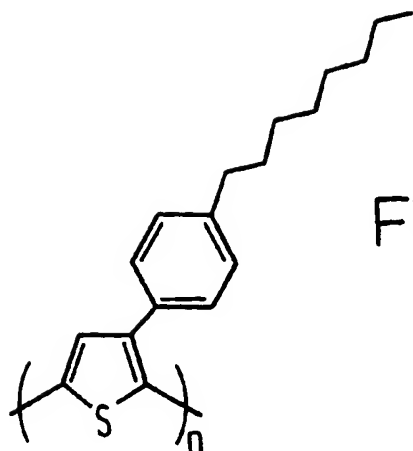


FIG. 1

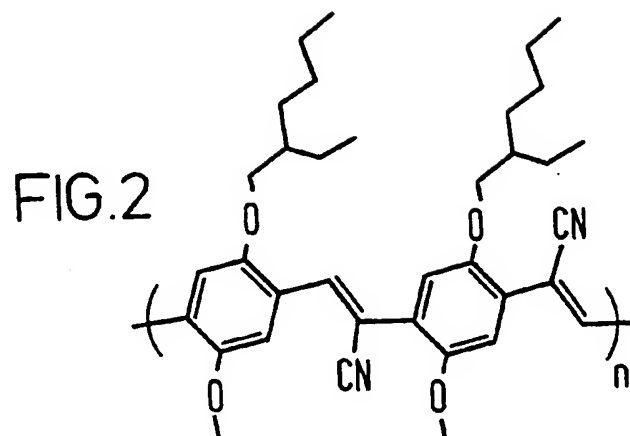


FIG. 2

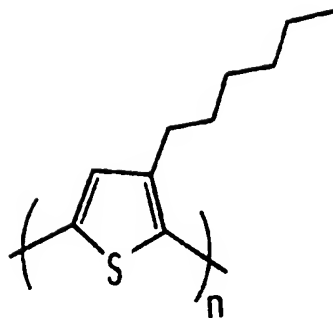


FIG. 3

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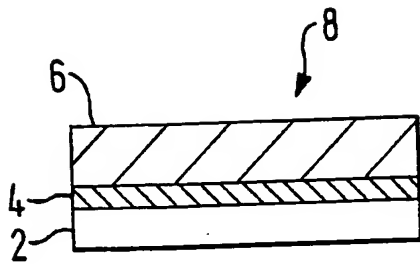


FIG. 4a

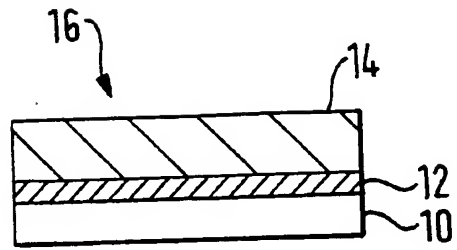


FIG. 4b

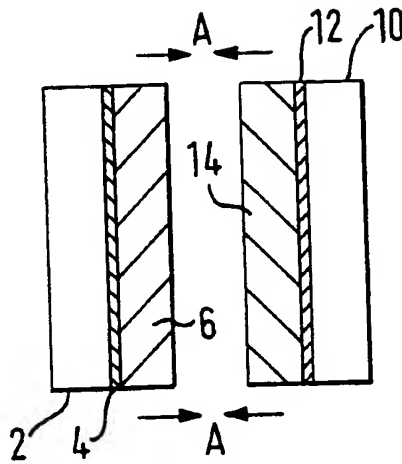


FIG. 4c

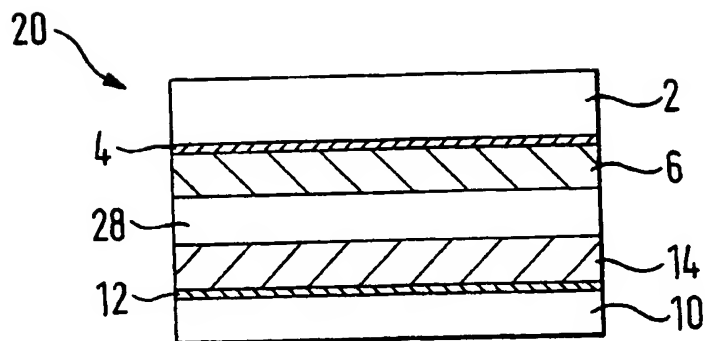


FIG. 5

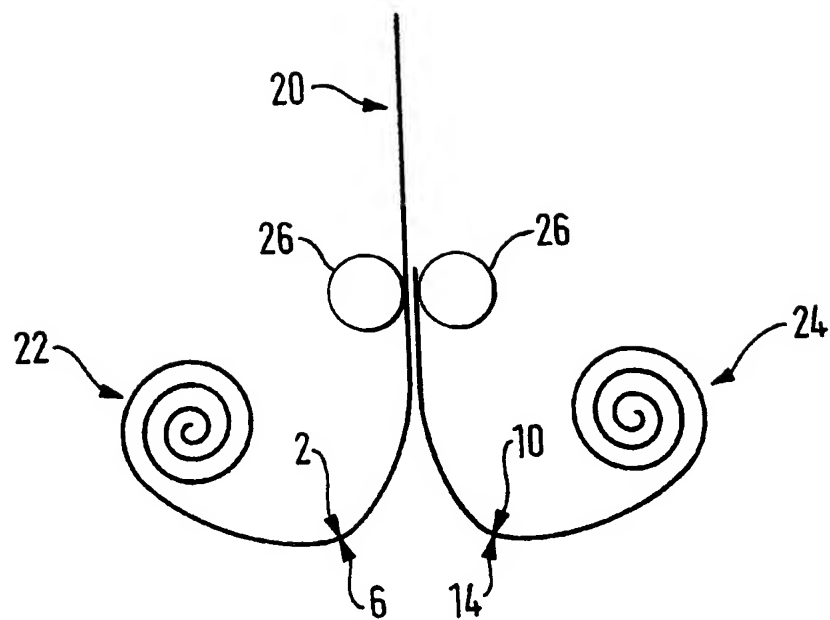


FIG. 6

INTERNATIONAL SEARCH REPORT

Int. Application No.

PCT/GB 99/00349

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H01L51/20 H01L51/30 H01L51/40

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Y	<p>WO 96 33593 A (CAMBRIDGE DISPLAY TECH ;FRIEND RICHARD HENRY (GB); HOLMES ANDREW B) 24 October 1996</p> <p>see page 9, line 26 - page 10, line 20; claims 1-13; figures 1-9 see page 4, line 19 - page 5, line 3 see page 6, line 33 - page 8, line 21</p> <p style="text-align: center;">--- -/--</p>	<p>1-4, 7, 9-26, 34, 37, 39, 40, 42, 43, 45, 47-49</p>
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

7 May 1999

Date of mailing of the international search report

26/05/1999

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Visentin, A

INTERNATIONAL SEARCH REPORT

International Application No
PCT/GB 99/00349

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Information on patent family members

International Application No

PCT/GB 99/00349

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US 5670791 A	23-09-1997	EP 0740855 A WO 9616449 A JP 9508504 T	06-11-1996 30-05-1996 26-08-1997